

Amendments to the Specification:

Please amend paragraph [0003] as follows:

[0003] Various computer programs have been proposed to assist in the design of an integrated circuit and more particularly in the testing of a design. These computer programs have typically been in the form of digital simulators which simulate a circuit under test. A hardware description language ([HDD]) HDL has been designed to simulate and describe the behavior of digital circuitry. However, whilst programs such as HDL are useful in testing a design, they do not assist in the design of the integrated circuit itself.

Please amend paragraph [0042] as follows:

[0042] The packet also has an 8 bit field F3 which identifies the type of transaction. In other words, this 8 bit field contains the op-code. One of the bits of op-code field defines the packet as being a request packet or a response packet. For other bit positions in the op-code field F3 of the request packet the size and type of the transaction are defined. For example, the code [[m]] may define the transaction as being a read or a write transaction if the request packet is intended for a memory interface target or a similar target.